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| APPLICATION NO. | FILING DATE | FIRST NAMED INVENTOR | ATTORNEY DOCKET NO. | CONFIRMATION NO. |
|-------------------------------------------------------------|-------------|----------------------|------------------------|------------------|
| 10/738,319 | 12/17/2003 | Patrick M. Bailey | LENX-0002 | 7917 |
| 27964 | 7590 | 11/02/2005 | EXAMINER | |
| HITT GAINES P.C. P.O. BOX 832570 RICHARDSON, TX 75083 | | | NATALINI, JEFF WILLIAM | |
| | | | ART UNIT | PAPER NUMBER |
| | | | 2858 | |

DATE MAILED: 11/02/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/738,319

Applicant(s)

BAILEY ET AL.

Examiner

Jeff Natalini

Art Unit

2858

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) OR THIRTY (30) DAYS, WHICHEVER IS LONGER, FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☒ Responsive to communication(s) filed on 18 August 2005.
- 2a) ☒ This action is **FINAL**. 2b) ☐ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-26 is/are pending in the application.
- 4a) Of the above claim(s) 8-26 is/are withdrawn from consideration.
- 5) ☐ Claim(s) _____ is/are allowed.
- 6) ☒ Claim(s) 1-7 is/are rejected.
- 7) ☐ Claim(s) _____ is/are objected to.
- 8) ☐ Claim(s) _____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 05 April 2004 is/are: a) ☐ accepted or b) ☒ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
2. ☐ Certified copies of the priority documents have been received in Application No. _____.
3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).
- * See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- 1) ☐ Notice of References Cited (PTO-892)
- 2) ☐ Notice of Draftsperson's Patent Drawing Review (PTO-948)
- 3) ☐ Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date _____
- 4) ☐ Interview Summary (PTO-413)
Paper No(s)/Mail Date. _____
- 5) ☐ Notice of Informal Patent Application (PTO-152)
- 6) ☐ Other: _____

DETAILED ACTION

Drawings

1. The drawings are objected to because the drawing needs to more specifically point out what is claimed. The centralized connector module needs to be properly shown with interconnected terminal sets (these in particular need to be clearly illustrated) with components, so that the interconnected terminal sets are configured to provide a common terminating point for said components during normal operation thereof. Corrected drawing sheets in compliance with 37 CFR 1.121(d) are required in reply to the Office action to avoid abandonment of the application. Any amended replacement drawing sheet should include all of the figures appearing on the immediate prior version of the sheet, even if only one figure is being amended. The figure or figure number of an amended drawing should not be labeled as "amended." If a drawing figure is to be canceled, the appropriate figure must be removed from the replacement sheet, and where necessary, the remaining figures must be renumbered and appropriate changes made to the brief description of the several views of the drawings for consistency. Additional replacement sheets may be necessary to show the renumbering of the remaining figures. Each drawing sheet submitted after the filing date of an application must be labeled in the top margin as either "Replacement Sheet" or "New Sheet" pursuant to 37 CFR 1.121(d). If the changes are not accepted by the examiner, the applicant will be notified and informed of any required corrective action in the next Office action. The objection to the drawings will not be held in abeyance.

Claim Rejections - 35 USC § 102

2. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

A person shall be entitled to a patent unless –

(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.

3. Claims 1, 3, and 7 are rejected under 35 U.S.C. 102(b) as being anticipated by Kanamori (6124716).

In regard to claim 1, Kanamori discloses a centralized connector module (the module includes in figure 1 (test connector (20); col 2 line 66-col 3 line 8) comprising a dielectric body (col 3 line 32-39, the body of the testing unit is a dielectric) having interconnected terminal sets (fig 1 (36)) corresponding to components connectable thereacross (fig 1, electrical device (24), states that multiple electrical devices can be tested simultaneously (col 1 line 67-col 2 line 3) ,said centralized connector module incapable of controlling said components (the device does not contain control means); and continuity indicator circuits associated with some of said terminals sets and configured to indicated continuity faults with respect to connected components (col 2 line 10-18 and abstract).

In regard to claim 3, Kanamori discloses wherein said interconnected terminal sets (figure 1 (36)) are configured to provide a common terminating point for said components during testing (the common terminating point is the circuit test unit (26)).

In regard to claim 7, Kanamori discloses wherein said terminal sets remain functional upon a failure of any of said continuity indicator circuits (col 1 line 64-col 2

line 18; abstract; also seen in figure 1 as the circuit test unit connects to the connection of the components on the board (12), but nothing is unplugged during testing so that the components would still operate).

Claim Rejections - 35 USC § 103

4. The following is a quotation of 35 U.S.C. 103(a) which forms the basis for all obviousness rejections set forth in this Office action:

(a) A patent may not be obtained though the invention is not identically disclosed or described as set forth in section 102 of this title, if the differences between the subject matter sought to be patented and the prior art are such that the subject matter as a whole would have been obvious at the time the invention was made to a person having ordinary skill in the art to which said subject matter pertains. Patentability shall not be negated by the manner in which the invention was made.

Claims 2, 4, and 6 are rejected under 35 U.S.C. 103(a) as being unpatentable over Kanamori (6124716) in view of Cheek et al. (3728616).

In regard to claims 2, 4, and 6, Kanamori lacks specifically stating that some of the terminal sets are connected in series and wherein the continuity circuits have impedances based on illuminating light emitting diodes, wherein voltage indicator circuits are associated with the output terminals sets to indicate an operation of corresponding ones of said components.

Cheek et al. discloses wherein the terminal sets are connected in series (fig 1 (R11 in series with R21 which is in series with R22, etc.)and wherein the continuity circuits have impedances based on illuminating light emitting diodes (col 3 line 35-41 and line 61- col 4 line 7), wherein voltage indicator circuits are associated with the output terminals sets to indicate an operation of corresponding ones of said components (abstract).

It would have been obvious to one with ordinary skill in the art at the time the invention was made for Kanamori to include terminal sets connected in series where a light is illuminated in association with the detection, wherein voltage indicator circuits are associated with output terminals of the components as taught by Cheek et al. in order to test for wiring errors in a plurality of pairs of terminals (abstract) so that it can be determined which particular connections are defective or fine (col 3 line 61 – col 4 line 7).

5. Claim 5 is rejected under 35 U.S.C. 103(a) as being unpatentable over Kanamori (6124716) in view of Collier et al. (6323652).

Kanamori discloses a power supply (junction box (10)- col 3 line 9-16)).

Kanamori lacks specifically wherein an AC Power In terminal set is associated with a voltage indicator circuit and configured to indicate a presence of a voltage thereacross.

Collier et al. discloses wherein an AC Power In terminal set (provided by an outlet) associated with a voltage indicator circuit and configured to indicate a presence of a voltage thereacross (col 8 line 1-3).

It would have been obvious to one with ordinary skill in the art at the time the invention was made for Kanamori to have an AC power in terminal set associated with a voltage indicator circuit as taught by Collier et al. in order to indicate that the level of the power supply is adequate (col 8 line 2-3).

Response to Amendment

6. In the response to this office action please point out exactly where support for amended claim 3 is found in the disclosure.

Response to Arguments

7. Applicant's arguments with respect to claims 1-7 have been considered but are moot in view of the new ground(s) of rejection.

Conclusion

8. Applicant's amendment necessitated the new ground(s) of rejection presented in this Office action. Accordingly, **THIS ACTION IS MADE FINAL**. See MPEP § 706.07(a). Applicant is reminded of the extension of time policy as set forth in 37 CFR 1.136(a).

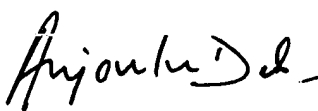
A shortened statutory period for reply to this final action is set to expire **THREE MONTHS** from the mailing date of this action. In the event a first reply is filed within **TWO MONTHS** of the mailing date of this final action and the advisory action is not mailed until after the end of the **THREE-MONTH** shortened statutory period, then the shortened statutory period will expire on the date the advisory action is mailed, and any extension fee pursuant to 37 CFR 1.136(a) will be calculated from the mailing date of the advisory action. In no event, however, will the statutory period for reply expire later than **SIX MONTHS** from the date of this final action.

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Jeff Natalini whose telephone number is 571-272-2266. The examiner can normally be reached on M-F 8-5.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Eddie Lefkowitz can be reached on 571-272-2180. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

Jeff Natalini



ANJAN DEB
PRIMARY EXAMINER